

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
MEARS ET AL.)
Serial No. 10/717,374)
Filing Date: NOVEMBER 11, 2003) Attorney Docket No.
62601_CON3
For: SEMICONDUCTOR DEVICE INCLUDING)
BAND-ENGINEERED SUPERLATTICE)
_____)

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. A discrepancy has been discovered. The filing receipt in error does not list the name and address of the Assignee. The correct name and address of the Assignee is RJ Mears, LLC; Waltham, MA.

In view of the error noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,



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SEP. 10. 2004 9:51AM

NO. 776 P. 3/5

In re Patent Application of:
MEARS ET AL.
Serial No. 10/717,374
Filed: NOVEMBER 19, 2003

CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO
THE FILING RECEIPT for U.S. Serial No. 10/717,374 is being
submitted via telefacsimile no. 703-746-9195 to: OFFICE OF
INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O.
BOX 1450 ALEXANDRIA, VA 22313-1450, this 10th day of
September, 2004.



SEP. 10. 2004 9:51AM

NO. 776

P. 4/5

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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/717,374	11/19/2003	2811	529	62601_CON3	9	36	2

Christopher F. Regan,
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CONFIRMATION NO. 6717

UPDATED FILING RECEIPT



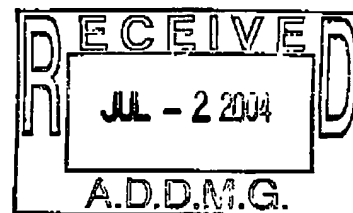
OC000000013090618

Date Mailed: 06/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Robert J. Mears, Wellesley, MA;
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 Marek Hytha, Brookline, MA;
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Assignee *RJ Mears, LLC; Waltham, MA*

Domestic Priority data as claimed by applicant

This application is a CON of 10/647,060 08/22/2003
 which is a CIP of 10/603,696 06/26/2003
 and is a CIP of 10/603,621 06/26/2003

Foreign Applications

If Required, Foreign Filing License Granted: 03/02/2004

Projected Publication Date: 12/30/2004

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Semiconductor device including band-engineered superlattice

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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